

Form PTO-1449

Attorney Docket No.
050324-1322Serial No.
TBA**INFORMATION DISCLOSURE CITATION***(Use several sheets if necessary)*Applicant
Lee, et al.Filing Date
HerewithGroup
TBA**U.S. PATENT DOCUMENTS**

Examiner Initials	Item	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
TDC	1	4,931,749	6/5/99	Walters			10/17/88
TDC	2	5,066,870	11/19/91	Kobatake			6/20/90
TDC	3	5,233,314	8/3/93	McDermott et al.			10/13/92
TDC	4	5,392,205	2/21/95	Zavaleta			11/7/91
TDC	5	5,920,233	7/6/99	Denny			11/18/96
TDC	6	5,491,439	2/13/96	Kelkar et al.			8/31/94
TDC	7	5,629,650	5/13/97	Gersbach et al.			1/29/96
TDC	8	5,646,563	9/30/96	Kuo			7/8/97
TDC	9	5,692,164	11/25/97	Pantelakis			12/18/96
TDC	10	5,703,511	12/30/97	Okamoto			10/23/96
TDC	11	5,740,213	4/14/98	Dreyer			6/3/94
TDC	12	5,767,736	6/16/98	Lakshmikumar et al.			5/27/97
TDC	13	5,870,003	2/9/99	Boerstler			10/3/97
TDC	14	5,886,551	3/23/99	Narahara			3/25/97
TDC	15	5,936,445	8/10/99	Babanezhad et al.			3/21/97
TDC	16	6,011,822	1/4/00	Dreyer			4/17/97
TDC	17	6,028,473	2/22/00	Kamei et al.			3/9/95
TDC	18	6,028,780	2/22/00	Chang			3/12/98
TDC	19	6,058,033	5/2/00	Williams et al.			10/8/98
TDC	20	6,064,251	5/16/00	Park			8/27/97
TDC	21	6,067,336	5/23/00	Peng			10/1/98
TDC	22	6,075,406	6/13/00	Lee et al.			6/16/98

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, etc.)*

* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

EXAMINER'S SIGNATURE:

T. Cunningham

DATE CONSIDERED:

12/15/03

Patent and Trademark Office; U. S. DEPARTMENT OF COMMERCE

Serial No.
TBA

INFORMATION DISCLOSURE CITATION

Applicant
Lee, et al.

(Use several sheets if necessary)


Filing Date
Herewith

Group
TBA

U.S. PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	30	<p>"Fully-Integrated CMOS Phase-Locked Loop with 15 to 240 MHz Locking Range and ± 50ps Jitter," Ilya Novof, John Austin, Russ Chmela, Todd Frank, Ram Kelkar, Ken Short, Don Strayer, Mark Srydhar, Steve Wyatt, ISSCC95/Session 6/Digital Design Elements/Paper TA 6.5</p>
	31	<p>"Digital Clocks and Latches," Ian Young, ISSCC96/February 9, 1996/Sea Cliff/8:30 a.m.</p>

* **EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

EXAMINER'S SIGNATURE:

DATE CONSIDERED:

E: T. Cunningham Patent

CONSIDERED:
12/15/02